Vectrawave
RF & Millimeter Wave MMIC Design

8.5 to 10.5GHz - 18W
GaN Power Amplifier

VM090S

General Description

The VM090S is a packaged power amplifier matched through 50Ω RF accesses. It can provide an output power up to 18W and associated power added efficiency of 29% in pulsed mode.

The VM090S is offered a hermetically sealed 10 leads 8 x 8 QFN designed to a surface mount design board. The QFN has a CuW base for superior thermal management. The VM090S integrates the VM090D VectraWave HPA.

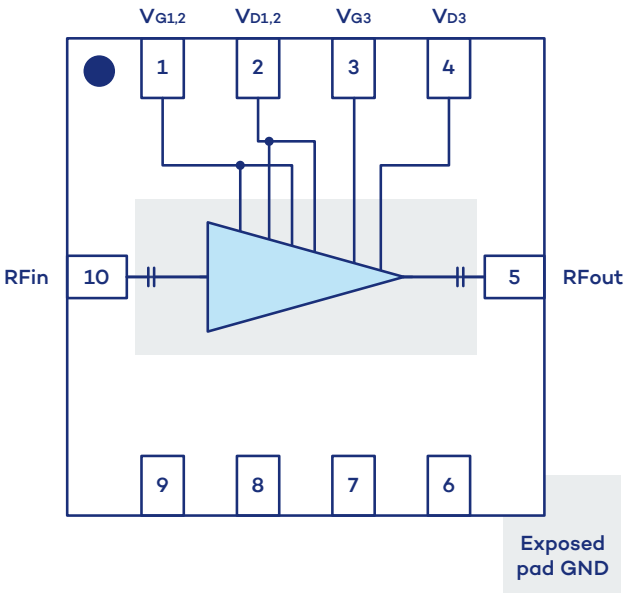
Features

Frequency range	**8.5 – 10.5GHz**
Output Power	**42.6dBm @Pin = 23dBm**
PAE	**29% @Pin = 23dBm**
Linear Gain	**27dB**
DC bias	**V_D = +28V, I_{DQ} = 190mA, V_G = -2.35V (Typical)**
Hermetic QFN	**8 x 8 (mm) 10leads**

Applications

- Radar
- Test and Measurement

Pins Assignement & Functional Block Diagram



Function	Pin number
$V_{G1,2}$	1
$V_{D1,2}$	2
V_{G3}	3
V_{D3}	4
RFout	5
NC	6
NC	7
NC	8
NC	9
RFin	10

Electrical Specifications

Test conditions: unless otherwise noted
- T_{amb} = +25°C
- V_D = +28V
- I_{DQ} = 190mA (V_G=-2.35V Typ.)
- Pulsed mode (pulse width: 30µs, duty cycle: 10%)

Symbol	Parameter	Min	Typ	Max	Unit
F	Frequency range	8.5		10.5	GHz
G	Linear gain		27		dB
S11	Input return loss		-10		dB
S22	Output return loss		-12		dB
Pout	Output power (@Pin=23dBm)		42.6		dBm
PAE	Associated Power Added Efficiency (@Pin=23dBm)		29		%
I_D	Associated Drain current (@Pin=23dBm)		2.4		A
V_D	Drain voltage		28		V

Recommended Operating Conditions

Symbol	Parameter	Value	Unit
V_D	Drain voltage	28	V
I_{DQ}	Drain quiescent current	190	mA
V_G	Gate voltage	-2.35 (Typ.)	V

Absolute Maximum Ratings

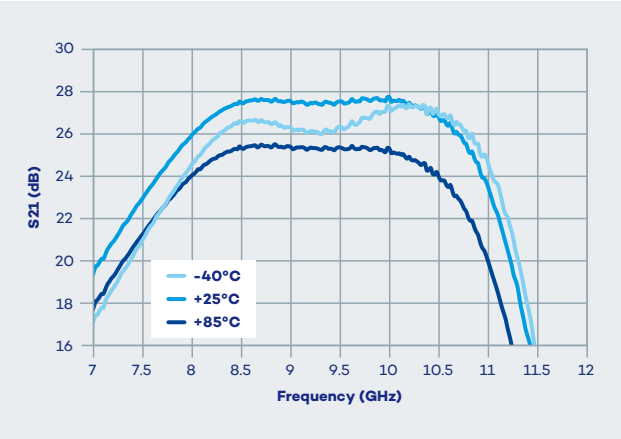
Symbol	Parameter	Value	Unit
V_D	Drain bias voltage	35	V
I_D	Drain bias current	3	A
V_G	Gate bias voltage	-10 to -2	V
Pin	Maximum peak input power overdrive	30	dBm
Tj	Junction temperature	225	°C
Ta	Operating temperature range	-40/+85	°C
Tstg	Storage temperature range	-55/+150	°C

Operation of this device above any of these parameters may cause permanent damage.

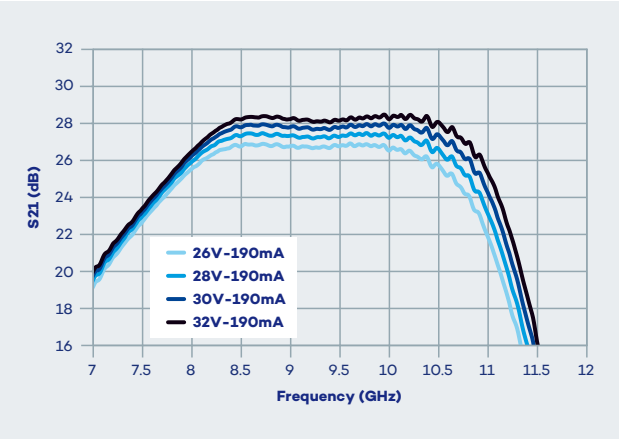
Typical Performance
(Small signal / Board Measurement)

Test conditions: unless otherwise specified
- Reference plane: connector access
- V_D = +28V
- I_{DQ} = 190mA (V_G = -2.35V Typ.)
- Pin = -20dBm

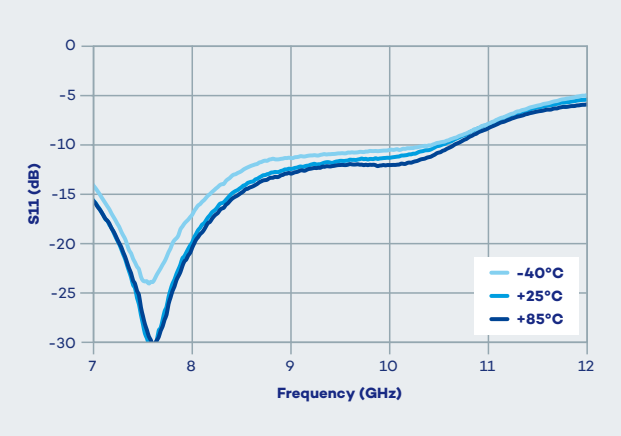
Gain vs Frequency vs Temperature



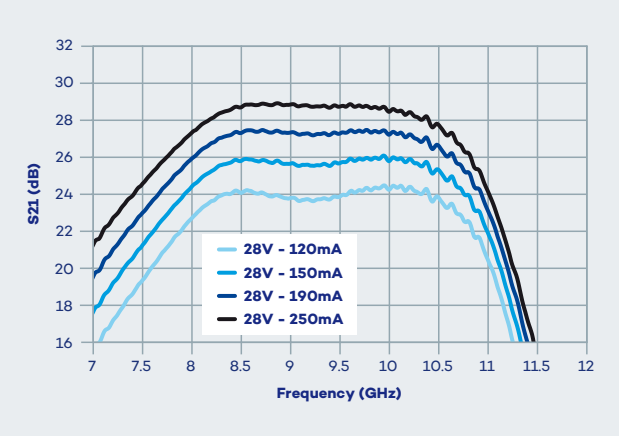
Gain vs Frequency vs V_D



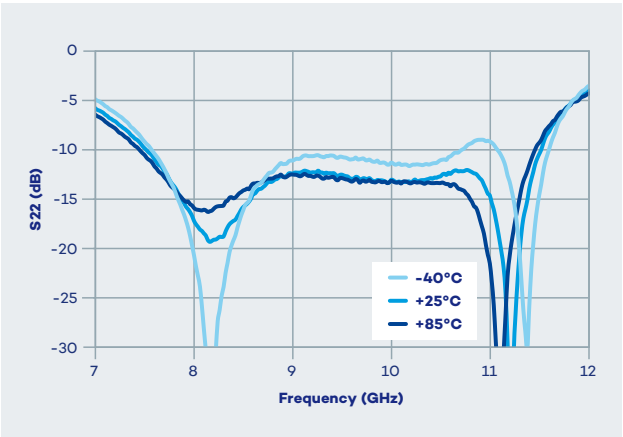
Input Return Loss vs Frequency vs Temperature



Gain vs Frequency vs I_{DQ}



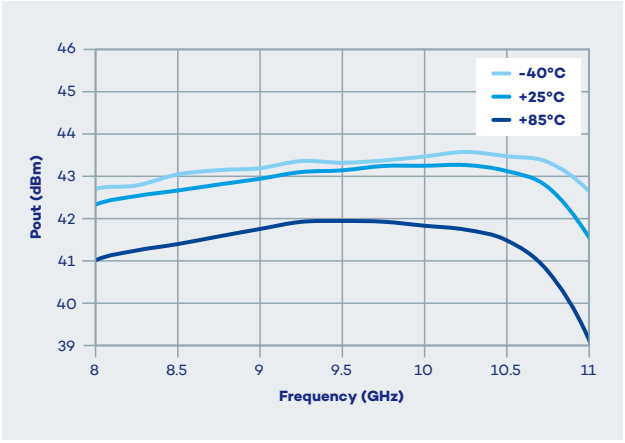
Output Return Loss vs Frequency vs Temperature



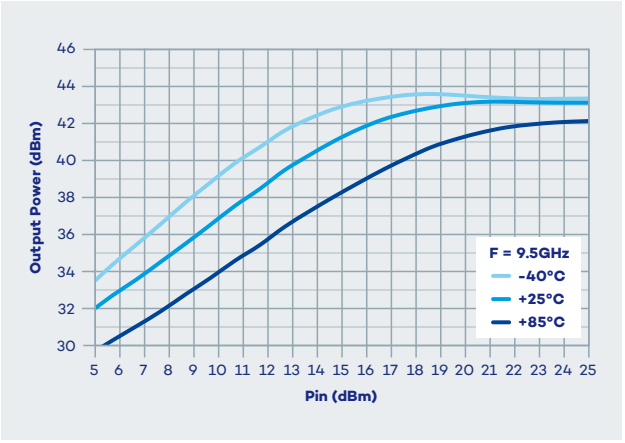
Typical Performance
(Large signal / Board Measurement)

Test conditions: unless otherwise specified
- Reference plane: component access
- V_D = +28V
- I_{DQ} = 190mA (V_G = -2.35V Typ.)
- Pin = +23dBm
- Pulsed mode (pulse width: 30µs, duty cycle: 10%)

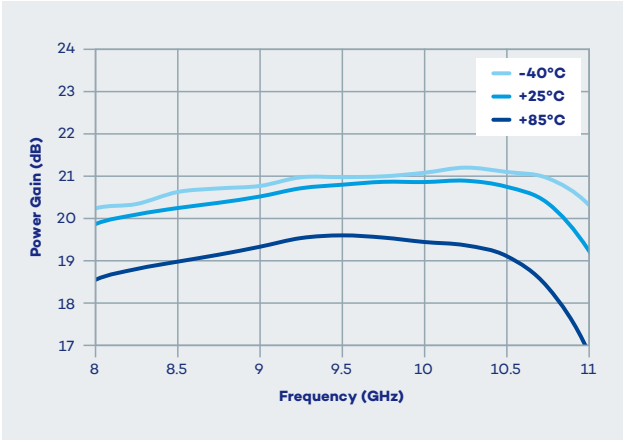
Output Power vs Frequency vs Temperature



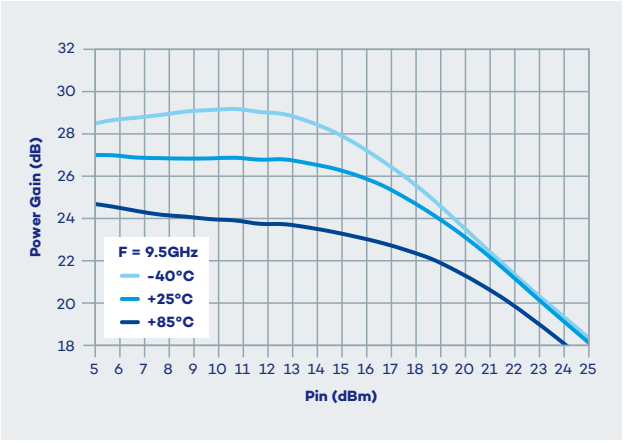
Output Power vs Input Power vs Temperature



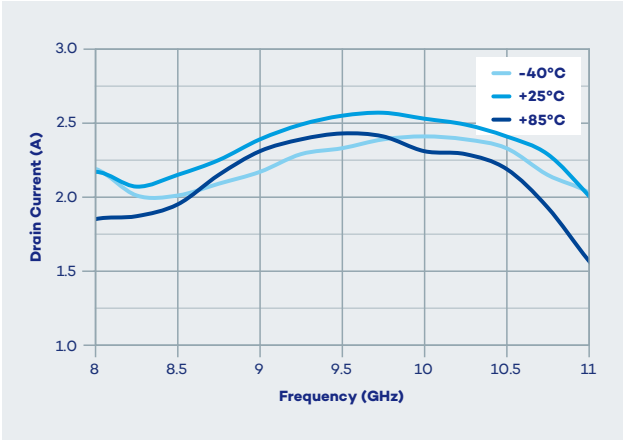
Power Gain vs Frequency vs Temperature



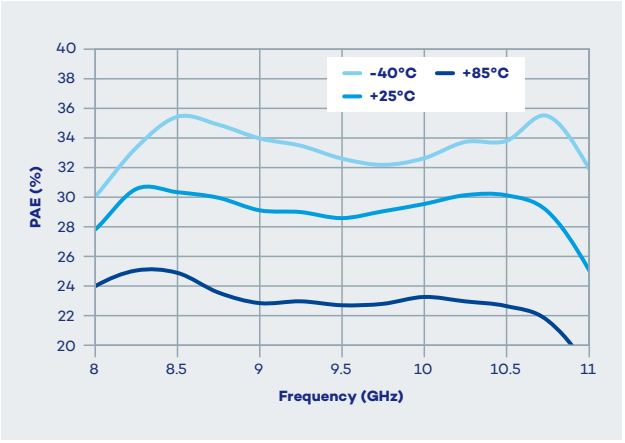
Gain vs Input Power vs Temperature



Drain Current vs Frequency vs Temperature



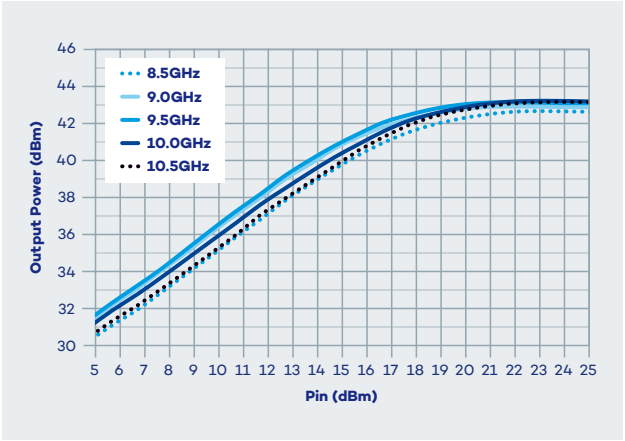
PAE vs Frequency vs Temperature



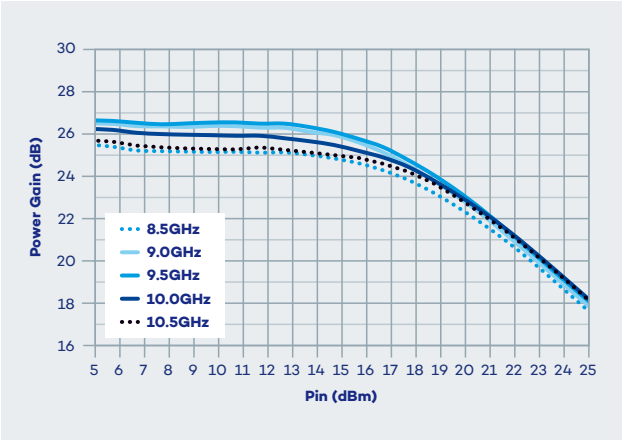
Typical Performance
(Large signal / Board Measurement)

Test conditions: unless otherwise specified
- Reference plane: component access
- V_D = +28V
- I_{DQ} = 190mA (V_G = -2.35V Typ.)
- T_{amb} = +25°C
- Pulsed mode (pulse width: 30µs, duty cycle: 10%)

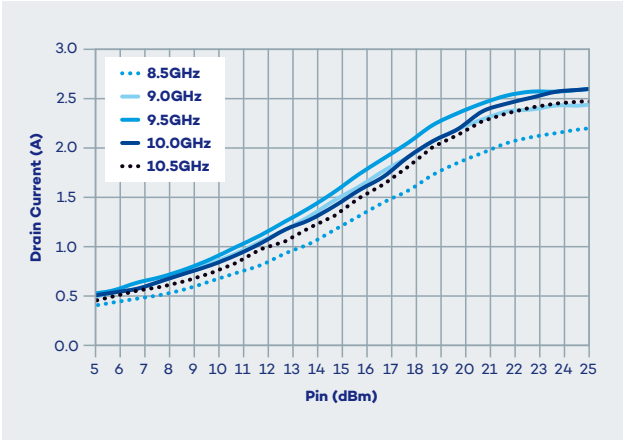
Output Power vs Input Power vs Frequency



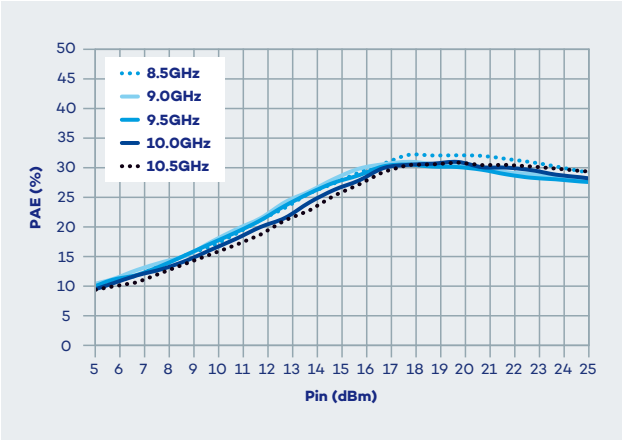
Gain vs Input Power vs Frequency



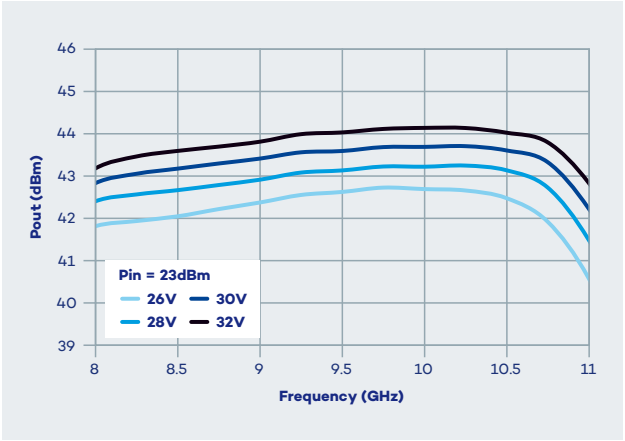
Drain Current vs Input Power vs Frequency



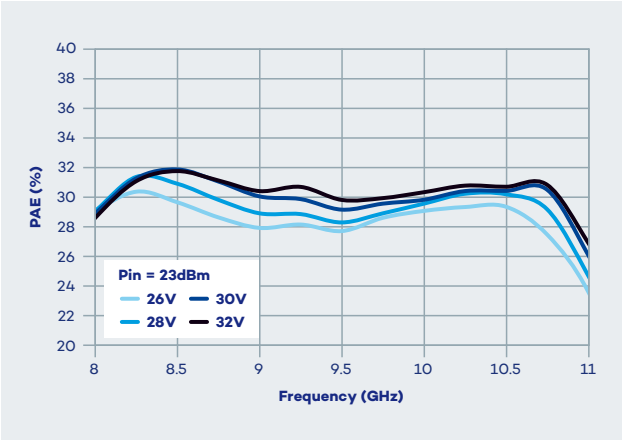
PAE vs Input Power vs Frequency



Output Power vs Frequency vs V_D



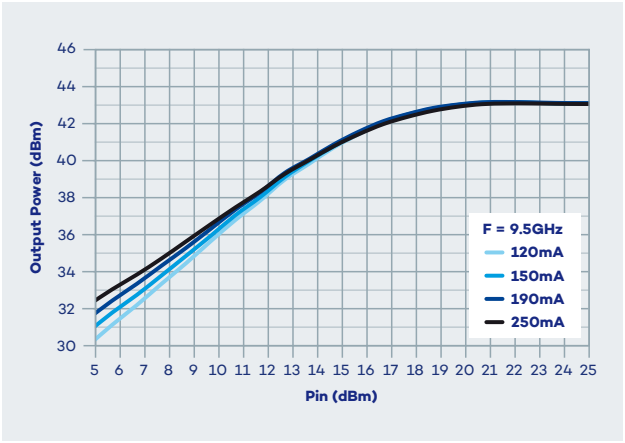
PAE vs Frequency vs V_D



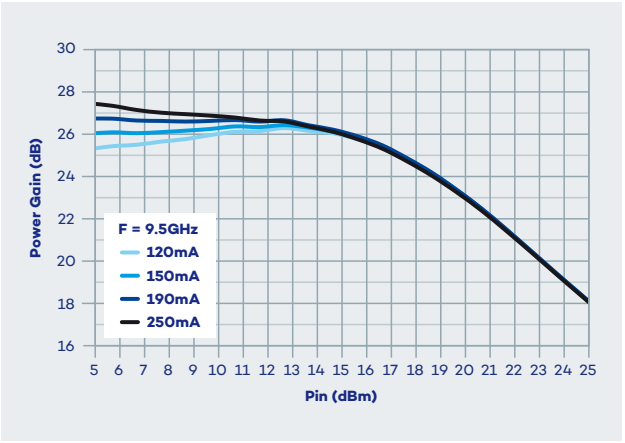
Typical Performance
(Large signal / Board Measurement)

Test conditions: unless otherwise specified
- Reference plane: component access
- V_D = +28V
- T_{amb} = +25°C
- Pulsed mode (pulse width: 30µs, duty cycle: 10%)

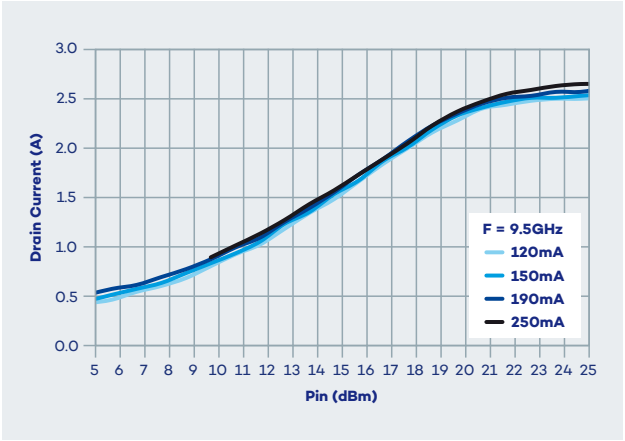
Output Power vs Input Power vs I_{DQ}



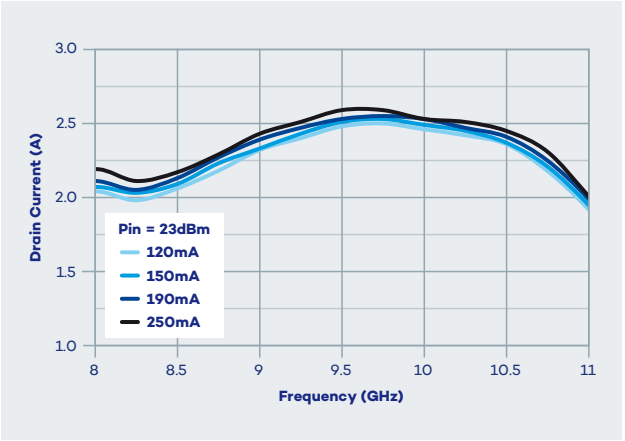
Gain vs Input Power vs I_{DQ}



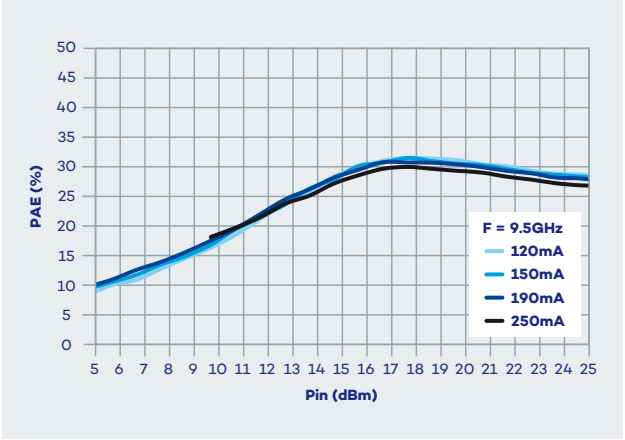
Drain Current vs Input Power vs I_{DQ}



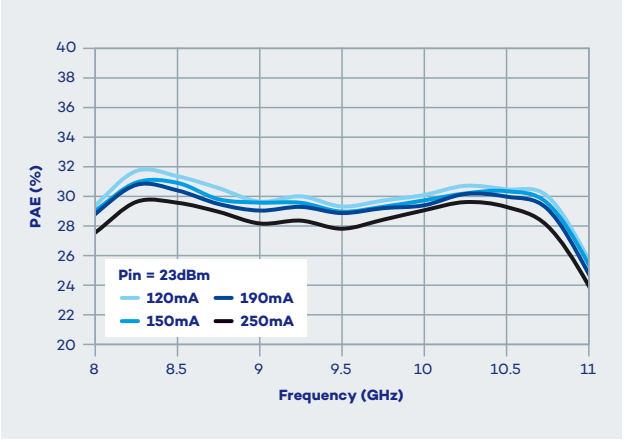
Drain Current vs Frequency vs I_{DQ}



PAE vs Input Power vs I_{DQ}

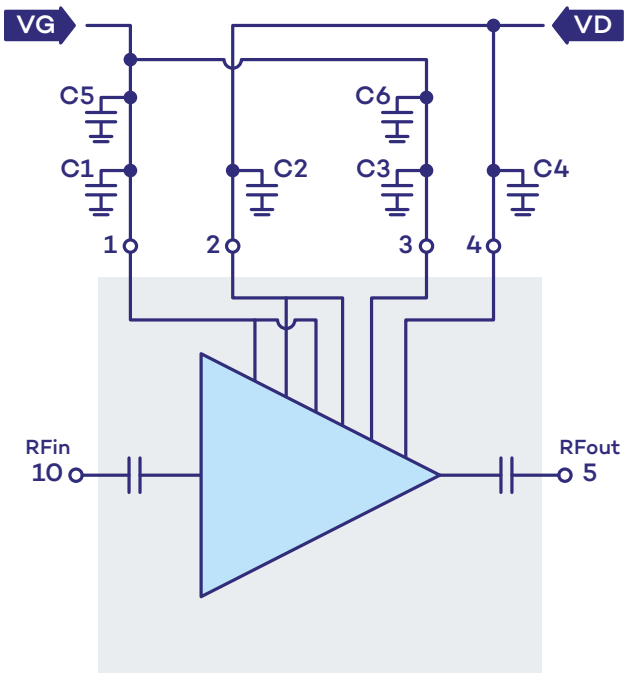


PAE vs Frequency vs I_{DQ}



Application circuit

- C1 to C4 = 1µF (50V/0603)
- C5, C6 = 100µF (16V/0805)



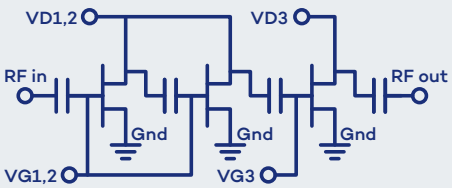
Bias-up procedure

1. Apply V_G = -3V
2. Apply V_D = +28V
3. Adjust V_G to obtain the specificied I_{DQ} = 190 mA (V_G = -2.35V Typ.)
4. Apply RF signal in pulsed mode

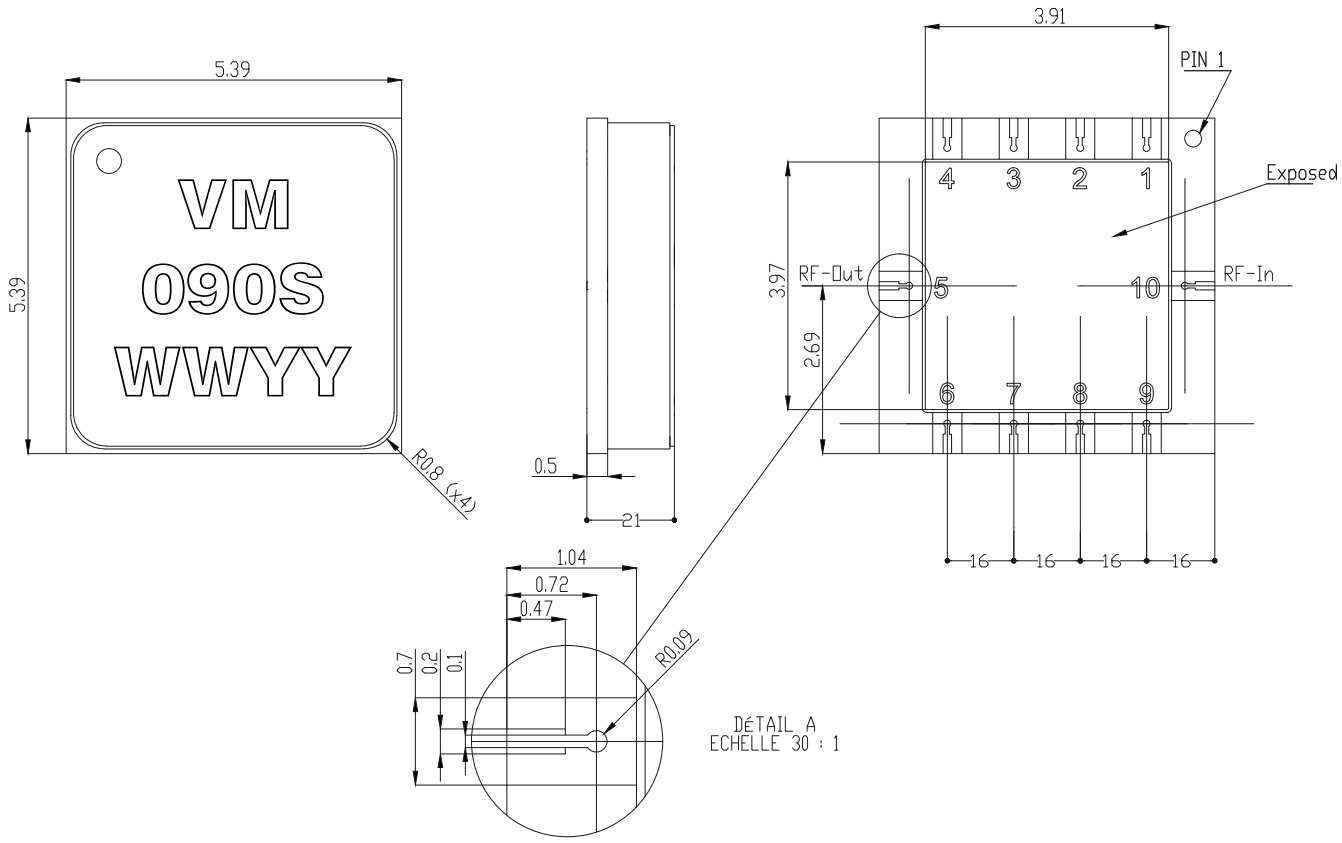
Bias-down procedure

1. Turn off RF signal
2. Reduce V_G = -3V
3. Apply V_D = 0V
4. Turn off power supply

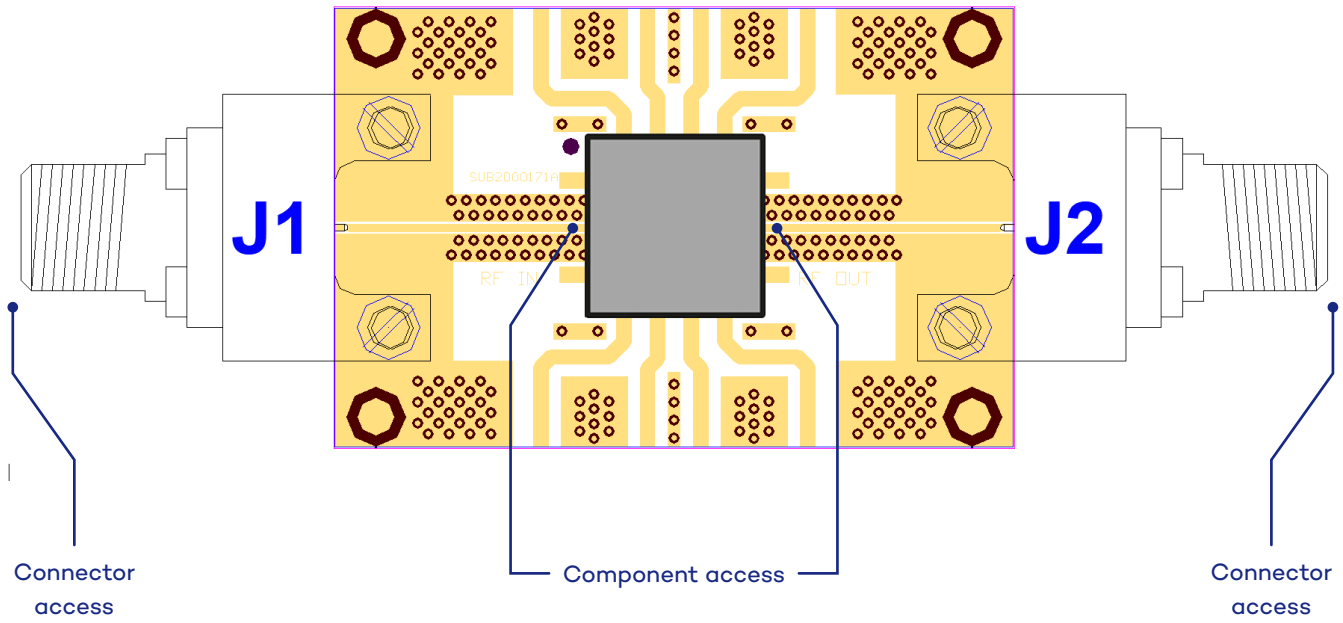
Pin description

Pin number	Name	Description	Electrical interface
10	RF in	Amplifier input, this access is AC coupled and internally matched to 50 Ohms	
1, 3	$V_{G1,2}$, V_{G3}	HPA Gate biasing input accesses	
2, 4	$V_{D1,2}$, V_{D3}	HPA Drain biasing input accesses	
5	RF out	Amplifier output, this access is AC coupled and internally matched to 50 Ohms	
Exposed Pad	Gnd	Die must be connected to RF and DC Ground	

Mechanical drawing



Evaluation Board (EVB) Layout Assembly



Ordering information

Product Code	Parameter
VM090S	8.5 to 10.5GHz - 18W GaN Power Amplifier Hermetic QFN 8 x 8 (mm) 10leads

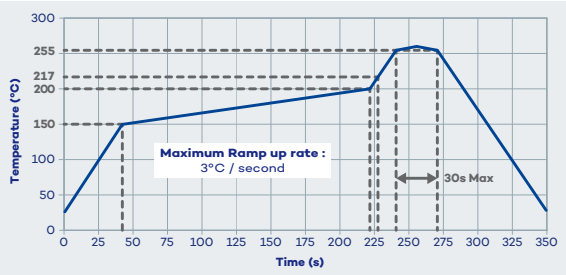
Associated Material

- Evaluation Board
- Mechanical files (DXF)

Product Compliance Information

Solderability

Solder Stencil thickness: 127µm
Solder: SAC 305 (ROHS)
Temperature profile example: maximum recommended reflow profile (leadfree)



ESD Sensitivy Rating

Test: Human Body Model (HBM)
Std: JEDEC Standard JESD22-A114

RoHS-Compliance

This part is compliant with EU 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment).

Other attributes

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C15H12Br402) Free
- PFOS Free
- SVHC Free

Contact information

For the latest specifications, additional product information, worldwide sales and distribution locations, and information about Vectrawave.

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